

Theodore Andronikos

Curriculum Vitae

Personal Info

Date & Place of birth: April 3rd, 1968, Athens
Marital status: Married, 2 children
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Affiliation

Assistant Professor, Department of Informatics, Ionian University, Corfu, Greece

Education

1999-2000: Postdoctoral research at the School of Electrical and Computer Engineering, National Technical University of Athens.
1997: Ph.D. in Computer Science, Computer Science Division, School of Electrical and Computer Engineering, National Technical University of Athens.
1992: Degree in Electrical and Computer Engineering (9.09/10), Computer Science Division, School of Electrical and Computer Engineering, National Technical University of Athens (9,09/10).

Scholarships and Awards

1999-2000: I.K.Y. (National Scholarship Foundation) for postdoctoral research at the School of Electrical and Computer Engineering, National Technical University of Athens.
1994-1996: Bodosaki Foundation Grant.
1995: EPISEY Foundation Grant.
1987: I.K.Y. (National Scholarship Foundation) scholarship for admittance to the ECE department (7th).

Dissertations

PhD Thesis: "Automated Parallelization Of Sequential Algorithms", School of Electrical and Computer Engineering, National Technical University of Athens, 1997.
Supervisor: Prof. George Papakonstantinou.
Diploma Thesis: "Boolean Algebra Methodologies in Artificial Intelligence" School of Electrical and Computer Engineering, National Technical University of Athens, 1992.
Supervisor: Prof. George Papakonstantinou.

Monographs

- (M₁) Michalis Stefanidakis, Ioannis Papadakis, Theodore Andronikos, "OPEN LINKED DATA AND APPLICATIONS: A practical approach to Semantic Web", <http://www.kallipos.gr/>.

Articles in Refereed International Journals

- (J₁₇) Konstantinos Giannakis, Theodore Andronikos, "[Membrane automata for modeling biomolecular processes](#)", to appear in Natural Computing, Springer (Impact Factor: 0.757).
- (J₁₆) Konstantinos Giannakis, Christos Papalitsas, Kalliopi Kastampolidou, Alexandros Singh, Theodore Andronikos, "[Dominant Strategies of Quantum Games on Quantum Periodic Automata](#)", Computation (MDPI - Open Access Publishing), Volume 3, Issue 4, 2015, pages 586-599.
- (J₁₅) Konstantinos Giannakis, Theodore Andronikos, "[Use of Buchi automata and randomness for the description of biological processes](#)", International Journal of Scientific World, Science Publishing Corporation, Volume 3, No 1, 2015, pages 113-123.
- (J₁₄) Ioannis Riakiotakis, Florina M. Ciorba, Theodore Andronikos, George Papakonstantinou, "[Towards the optimal synchronization granularity for dynamic scheduling of pipelined computations on heterogeneous computing systems](#)", Concurrency and Computation: Practice and Experience, John Wiley & Sons (Impact Factor: 0.636), Volume 24, Issue 18, December 2012, pages 2221-2448.
- (J₁₃) Ioannis Riakiotakis, Florina M. Ciorba, Theodore Andronikos, George Papakonstantinou, "[Distributed Dynamic Load Balancing for Pipelined Computations on Heterogeneous Systems](#)", Parallel Computing, Elsevier (Impact Factor: 1.086), Volume 37, Issues 10-11, October-November 2011, pages 713-729.
- (J₁₂) Theodore Andronikos, Florina M. Ciorba, Ioannis Riakiotakis, George Papakonstantinou, Anthony T. Chronopoulos, "[Studying the impact of synchronization frequency on scheduling tasks with dependencies in heterogeneous systems](#)", Performance Evaluation, Elsevier (Impact Factor: 1.168), Volume 67, Issue 12, December 2010, pages 1324-1339.
- (J₁₁) Theodore Andronikos, Vassia Pavlaki, "[Multi Dimensional CTL and Stratified Datalog](#)", International Journal of Computer Science & Information Technology (IJCSIT), Volume 2, Number 1, February 2010, pages 29-53.
- (J₁₀) Christos Pavlatos, Alexandros C. Dimopoulos, Andrew Koulouris, Theodore Andronikos, Ioannis Panagopoulos, George Papakonstantinou, "[Efficient reconfigurable embedded parsers](#)", Computer Languages, Systems & Structures, Elsevier, Volume 35, Issue 2, July 2009, Pages 196-215.
- (J₉) Theodore Andronikos, Florina M. Ciorba, Panayiotis Theodoropoulos, Dimitrios Kamenopoulos, George Papakonstantinou, "[CRONUS: A platform for parallel code generation based on computational geometry methods](#)", Journal of Systems and Software, Elsevier Volume 81, Issue 8, pp. 1389-1405, August 2008.
- (J₈) Florina M. Ciorba, Ioannis Riakiotakis, Theodore Andronikos, George Papakonstantinou, Anthony T. Chronopoulos, "[Enhancing self-scheduling algorithms via synchronization and weighting](#)", Journal of Parallel and Distributed Computing, Elsevier, Volume 68, Issue 2, February 2008, Pages 246-264.
- (J₇) George Papakonstantinou, Ioannis Riakiotakis, Theodore Andronikos, Florina M. Ciorba, Anthony T. Chronopoulos, "Dynamic Scheduling for Dependence Loops on Heterogeneous Clusters", Neural, Parallel & Scientific Computations, vol. 14, no. 4, pp. 359-384, December 2006.
- (J₆) Florina M. Ciorba, Theodore Andronikos, George Papakonstantinou, "Adaptive Cyclic

- Scheduling of Nested Loops”, HERMIS Intl. Journal, Volume 8(electronic version), 2006.
- (J5) Theodore Andronikos, George Koletsos and George Stavrinou, "[An Alternative Tableau method for Propositional Linear Temporal Logic](#)", The Bulletin of Symbolic Logic, Association for Symbolic Logic, Volume 12, Issue 2, June 2006.
- (J4) Irène Guessarian, Eugénie Foustoucos, Theodore Andronikos, and Foto Afrati, "[On temporal logic versus datalog](#)", Theoretical Computer Science, Elsevier, Volume 303, Issue 1, 28 June 2003, Pages 103-133.
- (J3) George Papakonstantinou, Ioannis Drositis, Theodore Andronikos, "On the Parallelization of UET/UET-UCT Loops", Neural, Parallel & Scientific Computations, vol. 9, no. 3-4, pp. 279-318, September & December 2001.
- (J2) Theodore Andronikos, Nectarios Koziris, George Papakonstantinou, Panayiotis Tsanakas, "[Optimal Scheduling for UET/UET-UCT Generalized n-Dimensional Grid Task Graphs](#)", Journal of Parallel and Distributed Computing, Elsevier, vol. 57, pp. 140-165, May 1999.
- (J1) Theodore Andronikos, Nectarios Koziris, Zacharias Tsiatsoulis, George K. Papakonstantinou, Panayiotis Tsanakas, "[Lower Time and Processor Bounds for Efficient Mapping of Uniform Dependence Algorithms into Systolic Arrays](#)", Parallel Algorithms and Applications, Taylor & Francis Group, vol. 10, no. 3-4, pp. 177-194, 1997.

Book Chapters

- (B7) Konstantinos Giannakis, Theodore Andronikos, "[Mitochondrial Fusion Through Membrane Automata](#)", GeNeDis 2014, Springer Advances in Experimental Medicine and Biology, (Impact Factor: 1.825), Volume 820, pp. 163-172, ISBN: 978-3-319-09011-5 (Print) 978-3-319-09012-2 (Online), 2015.
- (B6) Panayiotis Tsanakas, Theodore Andronikos, Nectarios Koziris, George Papakonstantinou, "Scheduling UET Grids with Unit Communication Time Delays Into Unbounded/Fixed Number of Processors", Chapter 2, pp. 43-68, in Book/Volume 5, Advances in High Performance Computing Series: Highly Parallel Computations, Computational Mechanics Publications, UK, 2001.
- (B5) Theodore Andronikos, Stathis Zachos, "A Modal Logic Formalism for Scheduling Dags", in D. I. Fotiadis and S. D. Nikolopoulos (editors), Advances in Informatics, pp. 42-51, ISBN 981-02-4192-5, World Scientific, Singapore, 2000.
- (B4) Nectarios Koziris, Theodore Andronikos, George Economakos, George Papakonstantinou, Panayiotis Tsanakas, "Automatic Hardware Synthesis of Nested Loops using UET Grids and VHDL", High-Performance Computing and Networking: International Conference and Exhibition, Springer-Verlag Lecture Notes in Computer Science, vol. 1225, pp. 888-897, ISBN 3-540-62898-3, 1997.
- (B3) Foto Afrati, Theodore Andronikos, Theodore Kavalieros, "[On the Expressiveness of Query Languages with Linear Constraints: Capturing Desirable Spatial Properties](#)", Constraint Databases and Applications, Springer-Verlag Lecture Notes in Computer Science, vol. 1191/1997, pp. 105-115, ISBN 3-540-62501-1, 1997.
- (B2) Foto Afrati, Theodore Andronikos, Theodore Kavalieros, "[On the Expressiveness of First-Order Constraint Languages](#)", Constraint Databases, Springer-Verlag Lecture Notes in Computer Science, vol. 1034/1996, pp. 22-39, ISBN 3-540-60794-3, 1996.
- (B1) Dimitris Papadias, Theodore Andronikos, "[Relation-Based Information Processing with Symbolic Spatial Images](#)", IGIS '94: Geographic Information Systems, Springer-Verlag Lecture Notes in Computer Science, vol. 884/1994, pp. 288-291, ISBN 3-540-58795-0, 1994.

Articles in Refereed International Conferences

- (C₃₉) Konstantinos Giannakis, Christos Papalitsas, Theodore Andronikos, "Quantum Automata for Infinite Periodic Words", 6th International Conference on Information, Intelligence, Systems and Applications (IISA), 6-8 July, Corfu, Greece, 2015.
- (C₃₈) Konstantinos Giannakis, Christos Papalitsas, Theodore Andronikos, Dimitrios Theotokis, Angelo Sifaleras, "Initialization methods for the TSP with Time Windows using Variable Neighborhood Search", 6th International Conference on Information, Intelligence, Systems and Applications (IISA), 6-8 July, Corfu, Greece, 2015.
- (C₃₇) Konstantinos Giannakis, Theodore Andronikos, "[Querying Linked Data and Büchi Automata](#)", IEEE Proceedings of the 9th International Workshop on Semantic and Social Media Adaptation and Personalization (SMAP), 6-7 November, Corfu, Greece, pp. 110 - 114, 2014.
- (C₃₆) Konstantinos Giannakis, Theodore Andronikos, "Mitochondrial Fusion Through Membrane Automata", Proceedings of the 1st World Congress on Geriatrics and Neurodegenerative Disease Research, (GeNeDis 2014), 10-13 April, Corfu, Greece, 2014.
- (C₃₅) Theodore Andronikos, Michalis Stefanidakis, Ioannis Papadakis, "[Adding Temporal Dimension to Ontologies via OWL Reification](#)", Proceedings of 13th Panhellenic Conference on Informatics - PCI 2009 Conference, 10-12 September, Corfu, Greece. IEEE Computer Society, pp. 19-22, 2009.
- (C₃₄) Florina M. Ciorba, Ioannis Riakiotakis, Theodore Andronikos, Anthony T. Chronopoulos, George Papakonstantinou, "[Optimal Synchronization Frequency for Dynamic Pipelined Computations on Heterogeneous Systems](#)", IEEE International Conference on Cluster Computing (CLUSTER 2007), Austin, TX USA, September 17-20, 2007.
- (C₃₃) Florina M. Ciorba, Ioannis Riakiotakis, Theodore Andronikos, Anthony T. Chronopoulos, George Papakonstantinou, "[Studying the impact of synchronization frequency on scheduling tasks with dependencies in heterogeneous systems](#)", 16th International Conference on Parallel Architectures and Compilation Techniques (PACT'07), Brasov, Romania, September 15-19, 2007.
- (C₃₂) Florina M. Ciorba, Ioannis Riakiotakis, Theodore Andronikos, George Papakonstantinou, "[Self-Adapting Scheduling for Tasks with Dependencies in Stochastic Environments](#)", Proceedings of the 5th International Workshop on Algorithms, Models and Tools for Parallel Computing on Heterogeneous Networks (HeteroPar'06), September 28, 2006, Barcelona, Spain.
- (C₃₁) Andrew Koulouris, Theodore Andronikos, Christos Pavlatos, Alexandros C. Dimopoulos, Ioannis Panagopoulos, George Papakonstantinou, "[Efficient Signal Processing using Syntactic Pattern Recognition Methods](#)", Proceedings of the IASTED Conference on Signal and Image Processing (SIP 2006), August 14-16, 2006, Honolulu, Hawaii, USA.
- (C₃₀) Florina M. Ciorba, Theodore Andronikos, Ioannis Riakiotakis, Anthony T. Chronopoulos, George Papakonstantinou, "[Dynamic Multi Phase Scheduling for Heterogeneous Clusters](#)", Proceedings of the 20th IEEE International Parallel & Distributed Processing Symposium (IPDPS 2006), April 25-29, 2006, Rhodes Island, Greece.
- (C₂₉) Theodore Andronikos, George Koletsos, George Stavrinos, "An Alternative Tableaux method for Propositional Linear Temporal Logic", Proceedings of the Logic Colloquium 2005 (LC 2005), Athens, Greece.
- (C₂₈) Florina M. Ciorba, Theodore Andronikos, Ioannis Drositis, George Papakonstantinou, Panayiotis Tsanakas, "[Reducing the Communication Cost via Chain Pattern Scheduling](#)", Proceedings of the 4th IEEE International Symposium on Network Computing and Applications (NCA 2005), pp. 186-193, July 27-29, 2005, Cambridge, MA USA.

- (C27) Florina M. Ciorba, Theodore Andronikos, George Papakonstantinou, "Adaptive Cyclic Scheduling of Nested Loops", Proceedings of the 7th Hellenic European Conference on Computer Mathematics and its Applications (HERCMA 2005).
- (C26) Theodore Andronikos, Florina M. Ciorba, Panayiotis Theodoropoulos, Dimitris Kamenopoulos, George Papakonstantinou, "[Code generation for General Loops using methods from Computational Geometry](#)", Proceedings of the 16th IASTED Parallel and Distributed Computing and Systems Conference (PCDS 2004), pp. 348-353, Cambridge, MA USA, November 9-11, 2004.
- (C25) Florina M. Ciorba, Theodore Andronikos, Dimitris Kamenopoulos, Panayiotis Theodoropoulos, George Papakonstantinou, "SimpleCode Generation for special UDLs", Proceedings of the 1st Balcan Conference on Informatics (BCI 2003), pp. 466-475, Thessaloniki, Greece, November 2003.
- (C24) Theodore Andronikos, Marios Kalathas, Florina M. Ciorba, Panayiotis Theodoropoulos, George Papakonstantinou, "An Efficient Scheduling of Uniform Dependence Loops", Proceedings of the 6th Hellenic European Conference on Computer Mathematics and its Applications (HERCMA 2003).
- (C23) Foto Afrati, Theodore Andronikos, Vassia Pavlaki, Eugenie Foustoucos, Irène Guessarian, "[From CTL to Datalog](#)", Principles of Computing and Knowledge: Paris C. Kanellakis Memorial Workshop (PCK50), pp. 72-85, San Diego, June 2003.
- (C22) Theodore Andronikos, Marios Kalathas, Florina M. Ciorba, Panayiotis Theodoropoulos, George Papakonstantinou, Panayiotis Tsanakas, "[Scheduling nested loops with the least number of processors](#)", Proceedings of the 21st IASTED International Conference on Applied Informatics, pp. 713-718, Innsbruck, Austria, February 2003.
- (C21) Ioannis Drositis, Theodore Andronikos, Marios Kalathas, George Papakonstantinou, Nectarios Koziris, "Optimal Loop Parallelization in n-Dimensional Index Spaces", Proceedings of 2002 International Conference on Parallel and Distributed Processing Techniques and Applications (PDPTA 2002), pp. 2197-2201, Las Vegas, Nevada, USA, 2002.
- (C20) Ioannis Drositis, Theodore Andronikos, George Manis, George Papakonstantinou, Nectarios Koziris, "Geometric Scheduling of 2-D UET/UET-UCT Uniform Dependence Loops", Proceedings of the 10th Euromicro Workshop on Parallel, Distributed and Network-based Processing (PDP 2002), IEEE Press, pp. 343-350, Gran Canaria Island, Spain, 2002.
- (C19) Theodore Andronikos, Petros Potikas, Stathis Zachos, "A Distributed Temporal Logic Framework for the Verification and Synthesis of Reactive Systems", Proceedings of the 8th Hellenic Conference on Informatics, vol. II, pp. 1-9, Cyprus, November 2001.
- (C18) Foto Afrati, Theodore Andronikos, Eugenie Foustoucos, Irène Guessarian, "CTL et al. vs. Monadic Inf-Datalog", International Workshop on Logic and Complexity in Computer Science (LCCS 2001), pp. 17-36, Créteil, France, September 2001.
- (C17) Ioannis Drositis, Theodore Andronikos, Aneta Alexandridi, George Papakonstantinou, Nectarios Koziris, "Geometric Pattern Prediction and Scheduling of Uniform Dependence Loops", Proceedings of the 5th Hellenic European Conference on Computer Mathematics and its Applications (HERCMA 2001).
- (C16) Ioannis Drositis, Theodore Andronikos, Aggelos Kokorogiannis, George Papakonstantinou, Nectarios Koziris, "[Geometric Scheduling of 2-D Uniform Dependence Loops](#)", Proceedings of the 2001 IEEE International Conference on Parallel and Distributed Systems (ICPADS 2001), IEEE Press, pp. 259-264, Korea, 2001.
- (C15) Theodore Andronikos, Nectarios Koziris, George Papakonstantinou, Panayiotis Tsanakas, "[Optimal Scheduling for UET-UCT Grids Into Fixed Number of Processors](#)", Proceedings of 8th Euromicro Workshop on Parallel and Distributed Processing (PDP 2000), IEEE Press, pp. 237-243, Rhodes, Greece, 2000.

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- (C₁₄) Theodore Andronikos, Stathis Zachos, "A Modal Logic Formalism for Scheduling Dags", Proceedings of the 7th Hellenic Conference on Informatics, pp. II.1-II.8, Ioannina, Greece, August 1999.
- (C₁₃) Foto Afrati, Eugenie Foustoucos, Theodore Andronikos, "Datalog Trees and their Automata", Proceedings of the 2nd Panhellenic Logic Symposiums, Delphi, Greece, July 1999.
- (C₁₂) Andrew Koulouris, Nectarios Koziris, Theodore Andronikos, George Papakonstantinou, Panayiotis Tsanakas, "[A Parallel Parsing VLSI Architecture for Arbitrary Context Free Grammars](#)", Proceedings of the 1998 IEEE International Conference on Parallel and Distributed Systems (ICPADS 98), IEEE Press, pp. 783-790, Taiwan, ROC, Dec. 1998.
- (C₁₁) Andrew Koulouris, Nectarios Koziris, Theodore Andronikos, George Papakonstantinou, Panayiotis Tsanakas, "Automatic Generation of a VLSI Parallel Architecture for QRS Detection", Proceedings of the IX European Signal Processing Conference (EUSIPCO-98), vol. III, pp. 1597-1600, Rhodes, Greece, Sept. 1998.
- (C₁₀) Alexandros Pappas, Nectarios Koziris, Theodore Andronikos, George Papakonstantinou, Panayiotis Tsanakas, "LoopDep: An Integrated Environment for Nested Loop Parallelization using PVM", Proceedings of the 6th Hellenic Conference on Informatics, Greece, 1997.
- (C₉) Foto Afrati, Theodore Andronikos, Theodore Kavalieros, George Stavrinou, "Linear vs. Polynomial Queries", Proceedings of the 6th Hellenic Conference on Informatics, Greece, 1997.
- (C₈) Nectarios Koziris, George Economakos, Theodore Andronikos, George Papakonstantinou, Panayiotis Tsanakas, "[Optimal Automatic Hardware Synthesis For Signal Processing Algorithms](#)", Proceedings of 13th IEEE International Conference on Digital Signal Processing (DSP 97), IEEE Press, pp. 1011-1014, Santorini, Greece, 1997.
- (C₇) Nectarios Koziris, Theodore Andronikos, George Economakos, George Papakonstantinou, Panayiotis Tsanakas, "Automatic Hardware Synthesis of Nested Loops using UET Grids and VHDL", Proceedings of the International Conference on High Performance Computing and Networking (HPCN 97), Vienna, Austria, 1997.
- (C₆) Theodore Andronikos, Nectarios Koziris, George Papakonstantinou, Panayiotis Tsanakas, "[Optimal Scheduling for UET-UCT Generalized n-Dimensional Grid Task Graphs](#)", Proceedings of the 11th IEEE/ACM International Parallel Processing Symposium (IPPS 97), IEEE Press, pp. 146-151, Geneva, Switzerland, 1997.
- (C₅) Foto Afrati, Theodore Andronikos, Theodore Kavalieros, "On the Expressiveness of Query Languages with Linear Constraints; Capturing Desirable Spatial Properties", 2nd International CONTESSA Workshop on Constraint Databases (CDB '97), Delphi, Greece, 1997.
- (C₄) Theodore Andronikos, George Papakonstantinou, Panayiotis Tsanakas, "Optimal Scheduling of Uniform Dependence Loops in Polynomial Time", 2nd ECPD International Conference on Advance Robotics, Intelligent Automation and Active Systems, pp. 605-610, Vienna, Austria, 1996.
- (C₃) Foto Afrati, Theodore Andronikos, Theodore Kavalieros, "On the Expressiveness of First-Order Constraint Languages", 1st International CONTESSA Workshop on Constraint Databases, Germany, 1995.
- (C₂) Theodore Andronikos, Nectarios Koziris, Zacharias Tsiatsoulis, George Papakonstantinou, Panayiotis Tsanakas, "An Efficient Algorithm for the Optimal Linear Schedule of Uniform Dependence Algorithms", Proceedings of the 1st ECPD International Conference on Advanced Robotics and Intelligent Automation, pp. 140-145, Athens, 1995.
- (C₁) Dimitris Papadias, Theodore Andronikos, "Relation-Based Information Processing with Symbolic Spatial Images", Proceedings of the International Workshop on Advanced Research in Geographic Information Systems, IGIS94, Ascona, Switzerland, Springer-Verlag LNCS vol. 884, pp. 288-291, 1994.

Technical Reports

- (TR₄) Ioannis Drositis, Theodore Andronikos, George Papakonstantinou, "2-D Pattern Prediction and Scheduling", Technical Report CS-CSLAB-TR-01-01, Computing Systems Laboratory, NTUA, June 2001.
- (TR₃) Theodore Andronikos, Nectarios Koziris, Zacharias Tsiatsoulis, George Papakonstantinou, Panayiotis Tsanakas, "Low Complexity Methods for the Optimal Linear Time Schedule of Uniform Dependence Algorithms", Technical Report CS-DSCLAB-TR-3-95, Digital Systems and Computers Laboratory, NTUA, Mar. 1995.
- (TR₂) Theodore Andronikos, Nectarios Koziris, Zacharias Tsiatsoulis, George Papakonstantinou, Panayiotis Tsanakas, "Lower Bounds for Parallel Time and Number of Processors for the class of Uniform Dependence Algorithms", Technical Report CS-DSCLAB-TR-1-95, Digital Systems and Computers Laboratory, NTUA, Jan. 1995.
- (TR₁) Dimitris Papadias, Theodore Andronikos, "Information Processing With Symbolic Spatial Indexes", Technical Report CS-KDBSLAB-TR-07-93, Knowledge and Data Base Systems Laboratory, NTUA, Sep. 1993.

Invited Talks

- (IT₂) Theodore Andronikos, "Quantum Computers", 7th Conference on Informatics in Education, University of Piraeus, 9-11 October 2015.
- (IT₁) Theodore Andronikos, "Using Information Technologies to organize and process historical data", 1st Conference on Historical Research and History Teaching, Piraeus, November 2014.

Citations

- 205 citations, h-index: 9, g-index: 12 according to "[Publish or Perish](#)".
- 235 citations, 709 reads according to "[ResearchGate](#)".

Research Interests

- Internet programming.
- Query languages and inference engines for ontologies and the semantic the web.
- Quantum computation and quantum automata.
- Probabilistic automata and automata on infinite objects.
- Dynamic scheduling algorithms for parallel and distributed heterogeneous systems.
- Temporal logics for automated synthesis and verification of reactive systems.

Teaching Experience

2015 – Today	Department of Informatics, Ionian University, " Postgraduate Diploma of Specialization in Informatics " Master Course: <ul style="list-style-type: none">• Operating Systems
2009 – Today	Departments of History & Informatics, Ionian University Master Course: <ul style="list-style-type: none">• The use of new technologies in historical research
2009 – 2011	Department of Informatics, Ionian University Master Courses: <ul style="list-style-type: none">• Programming Principles

- 18/4/2007 – Today
 Department of Informatics, Ionian University
Courses:
- Artificial Intelligence
 - Introduction to Programming
 - Introduction to Computer Science
 - Computer Programming
 - Algorithms
 - Introduction to the Theory of Computation
 - Introduction to Quantum and DNA Computing
 - Compilers
 - Logic Programming
- 9/2006 – 17/4/2007
 Department of Informatics, Ionian University (Adjunct Lecturer)
Courses:
- Introduction to Programming
 - Introduction to Computer Science
 - Computer Programming
 - Compilers
- 10/2004 – 8/2006
 Department of Computer Science and Biomedical Informatics of the University of Central Greece (Adjunct Assistant Professor)
Courses:
- Introduction to Programming
 - Object-Oriented Programming
 - Algorithms and Complexity
- 10/2004– 8/2006
 Department of Information & Communications Systems Engineering, School Of Engineering, University of the Aegean (Adjunct Lecturer)
Courses:
- Programming Methodologies and Languages I
 - Programming Methodologies and Languages II
 - Software Engineering
- 3/2004 – 8/2004
 Department of Mathematics, University of Athens (Adjunct Lecturer)
Courses:
- Programming Languages
- 2/2001– 8/2004
 School of Applied Mathematics and Physics, National Technical University of Athens(Adjunct Lecturer)
Courses:
- Introduction to Programming
 - Program Design and Development
 - Programming Techniques
 - Programming Languages
 - Introduction to Computer Science
- 9/2001 – 7/2003
 Graduate Program in Logic, Algorithms and Computation (MPLA)
Master Courses:
- Algorithms and Complexity
 - Computational Complexity
- 1999– 2000
 School of Electrical and Computer Engineering, National Technical University of Athens in collaboration with Prof. F. Afrati
Graduate Course:
- Temporal Logics and Automata on Infinite Objects

Participation in Research Programmes

- 2011 – 2015** Scientific advisor and coordinator for the program: “On the Job Training of the Informatics Department of the Ionian University” (Greek Ministry of Education, Research and Religious Affairs).
- 2014 – 2015** ADRIATinn - a sustainable, cross-border ecosystem aiming to foster and boost SMEs R&D capacities and networking.
- 2013 – 2014** PACiNNO - Platform for trans-Academic Cooperation in Innovation.
- 2011 – 2013** Scientific advisor and coordinator for the program: “On the Job Training of the Informatics Department of the Ionian University” (Ministry of Education, Lifelong Learning and Religious Affairs).
- 2007 – 2009** Basic research program “Hardware/Software Co-design using High Level Synthesis Techniques, with focus on Pattern Recognition and Artificial Intelligence Applications”, PENED - 2003 Program for the Support of Research.
- 2005 – 2007** Basic research program "PYTHAGORAS II - Aid of Research Teams in the Universities", in the frame of the "Operational Program Education and Initial Professional Training (E.P.E.A.E.K. II)" program of the Ministry of Education, Lifelong Learning and Religious Affairs. The field of research was Automatic Mapping of Algorithms onto Embedded Systems.
- 2000 – 2002** Basic Research Program «Archimedes» entitled «System Verification & Synthesis using Temporal Logics and Automata».
- 2000 – 2002** Basic Research Program «Archimedes» entitled «Mathematical Logic Techniques in Programming».
- 1999 – 2001** Research Program «AGENDA-II: An High Level Meta-Compiling environment for High Performance Hardware», PENED 99 ED 521.
- 1996 – 1998** Research Program « Automated Parallelization Of Sequential Programs», PENED 1451.

Thesis and Dissertation Supervision

- Βασίλειος Λαπατάς. Διδακτορική Διατριβή: «The Web: Data Integration and Cutting Edge Applications», Τμήμα Πληροφορικής, Ιόνιο Πανεπιστήμιο, Ιανουάριος 2016.
Μέλος της επταμελούς εξεταστικής επιτροπής.
- Δημήτριος Καμενόπουλος. Διδακτορική Διατριβή: «Ποιότητα Υπηρεσίας στο Υπολογιστικό Πλέγμα», Σχολή Ηλεκτρολόγων Μηχανικών και Μηχανικών Υπολογιστών, Εθνικό Μετσόβιο Πολυτεχνείο, Ιούλιος 2009.
Μέλος της επταμελούς εξεταστικής επιτροπής.
- Μαρίνος Σαμψών. Διδακτορική Διατριβή: «Ελαχιστοποίηση Εκφράσεων Αποκλειστικού Ή - Κβαντικοί Αλγόριθμοι», Σχολή Ηλεκτρολόγων Μηχανικών και Μηχανικών Υπολογιστών, Εθνικό Μετσόβιο Πολυτεχνείο, Ιούλιος 2009.
Μέλος της επταμελούς εξεταστικής επιτροπής.
- Δημήτριος Ε. Βουδούρης. Διδακτορική Διατριβή: «Ελαχιστοποίηση Εκφράσεων Αποκλειστικού Ή», Σχολή Ηλεκτρολόγων Μηχανικών και Μηχανικών Υπολογιστών, Εθνικό Μετσόβιο Πολυτεχνείο, Απρίλιος 2008.
Μέλος της επταμελούς εξεταστικής επιτροπής.
- Florina M. Ciorba. Διδακτορική Διατριβή: "Algorithm Design for the Parallelization of Nested Loops", Σχολή Ηλεκτρολόγων Μηχανικών και Μηχανικών Υπολογιστών, Εθνικό Μετσόβιο Πολυτεχνείο, Φεβρουάριος 2008.
Επιβλέπων: Γεώργιος Παπακωνσταντίνου.
- Χρήστος Γ. Παυλάτος. Διδακτορική Διατριβή: «Αυτοματοποίηση Συσχεδίασης

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Referee in International Journals and Conferences

- Parallel Computing (Elsevier)
- Formal Methods in System Design (Elsevier)
- Journal of Systems and Software (Elsevier)
- The Journal of Supercomputing (Springer)
- International Journal of Pattern Recognition and Artificial Intelligence (World Scientific Publishing)
- Euro-Par 2002
- 11th IEEE International Parallel Processing Symposium (IPPS97)

Participation in Conference Programme Committees

- 2009: PCI 2009 - 13thPan-Hellenic Conference on Informatics, 10-12 Sep. 2009, Corfu, Greece. Member of the Organizing Committee.
- 6thInternational Conference on Database Theory (ICDT'97), Delphi, Greece, 1997. Member of the Organizing Committee.

Participation in Scientific Unions

- Member of the [Technical Chamber of Greece](#)

Languages

- CERTIFICATE of PROFICIENCY in ENGLISH - University of Cambridge